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APPLICANTS

Nao Miyamoto, Yokohama, JAPAN;

Toshiyuki Sakuta, Hamura, JAPAN;

** CONTINUING DATA *****

None Jan

** FOREIGN APPLICATIONS *****

JAPAN 2003-090212 03/28/2003

JAPAN 2003-172486 06/17/2003

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OK Jan

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged Examiner's Signature <i>[Signature]</i> Initials <i>[Initials]</i>	JAPAN	16	20	4

ADDRESS

020457
 ANTONELLI, TERRY, STOUT & KRAUS, LLP
 1300 NORTH SEVENTEENTH STREET
 SUITE 1800
 ARLINGTON, VA
 22209-3873

TITLE

Semiconductor integrated-circuit device and method to speed-up CMOS circuit

FILING FEE	FEES: Authority has been given in Paper	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time)
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